

# Integrating Dependency Schemes in Search-Based QBF Solvers

Florian Lonsing and Armin Biere

Institute for Formal Models and Verification (FMV)  
Johannes Kepler University, Linz, Austria  
<http://fmv.jku.at>

SAT'10  
July 11 - July 14, 2010  
Edinburgh, Scotland, United Kingdom



JOHANNES KEPLER  
UNIVERSITY LINZ | JKU

TNF

## Search-based QBF Solving for Prenex-CNF (PCNF): QDPLL

- Classical approach relies on linear quantifier prefix:  $Q_1 Q_2 \dots Q_n. \phi$ .
- Analyzing variable dependencies: can prefix order be relaxed?

### Example (Dependencies: quantifier ordering matters)

- $\forall x \exists y. (x = y)$  is satisfiable: value of  $y$  *depends* on value of  $x$ .
- $\exists y \forall x. (x = y)$  is unsatisfiable: value of  $y$  is fixed for all values of  $x$ .

### This Talk:

- Making QDPLL aware of dependencies.
- Identifying independence.
- Search-based QBF solving + dependency analysis.
- Implementation: DepQBF.

<i>Solver</i>	<i>Score</i>
<b>DepQBF</b>	<b>2896.68</b>
DepQBF-pre	2508.96
aqme-10	2467.96
qmaiga	2117.55
AlGSolve	2037.22
quantor-3.1	1235.14
struqs-10	947.83
nenofex-qbfeval10	829.11

QBFEVAL'10: score-based ranking.

[SS09, Bie04, BB07, Ben05]

**Dependency Schemes:**  $D \subseteq (V_{\exists} \times V_{\forall}) \cup (V_{\forall} \times V_{\exists})$ .

- General framework for expressing (in)dependence in PCNFs.
- $(x, y) \notin D$ :  $y$  independent from  $x$ .
- $(x, y) \in D$ : *conservatively* regard  $y$  as depending on  $x$ .

**$D$  as Directed-Acyclic Graph (Dependency-DAG):**

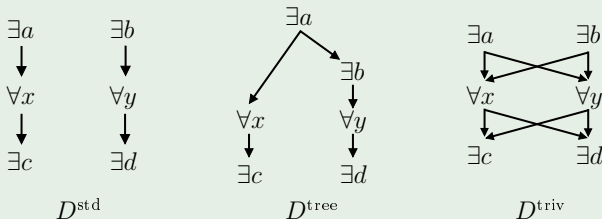
- Edges  $x \rightarrow y$  iff  $(x, y) \in D$ .

## Syntactic Approaches:

- Trivial dependency scheme  $D^{\text{triv}}$  (given prefix).
- Quantifier trees  $D^{\text{tree}}$ .
- Standard dependency scheme  $D^{\text{std}}$ .
- Theory:  $D^{\text{std}} \subseteq D^{\text{tree}} \subseteq D^{\text{triv}}$ .

## Example

$$\exists a, b \forall x, y \exists c, d. (a \vee x \vee c) \wedge (a \vee b) \wedge (b \vee d) \wedge (y \vee d).$$



**Goal:** dependency-DAG for  $D^{\text{std}}$  in QDPLL  $\Rightarrow$  expecting more freedom.

## Related Work:

- Classical result: description of QDPLL with  $D^{\text{triv}}$ .
- First generalization: QDPLL with  $D^{\text{tree}}$ .

## Our Results:

- Description of QDPLL for *arbitrary* dependency schemes.
- Solver DepQBF: implementation of QDPLL with  $D^{\text{std}}$ .
- Previous work: compact dependency-DAG representation for  $D^{\text{std}}$ .
- Experimental evaluation.

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
  R = get_initial_reason (s);
  // s == UNSAT: 'R' is empty clause.
  // s == SAT: 'R' is sat. cube...
  // ..or new cube from assignment.
  while (!stop_res (R))
    p = get_pivot (R);
    A = get_antecedent (p);
    R = constraint_res (R, p, A);
  add_to_formula (R);
  assign_forced_lit (R);
  return get_asserting_level (R);

```

**Figure:** QDPLL with conflict-driven clause and solution-driven cube learning.

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
  R = get_initial_reason (s);
  // s == UNSAT: 'R' is empty clause.
  // s == SAT: 'R' is sat. cube...
  // ..or new cube from assignment.
  while (!stop_res (R))
    p = get_pivot (R);
    A = get_antecedent (p);
    R = constraint_res (R, p, A);
  add_to_formula (R);
  assign_forced_lit (R);
  return get_asserting_level (R);

```

Figure: QDPLL with conflict-driven clause and solution-driven cube learning.

## Parts to be Generalized from $D^{\text{triv}}$ to Arbitrary $D \subseteq D^{\text{triv}}$ :

- Unit literal detection: expecting more units.
- Learning: expecting shorter constraints and “enabled” resolution steps.
- Stop criterion/asserting level: expecting longer backjumps.
- Selection of decision variables: expecting more freedom.

## Decisions and Dependency-Order:

- Out-of-order decisions: generally unsound  $\Rightarrow$  must branch in “ $D$ -order”.

### Example

$\exists a \forall x, y \exists b. \phi$ : branching on  $b$  possible by  $D^{\text{triv}}$  only if  $x, y$  assigned.

## Decision Candidates (DC):

- Def.: unassigned variables  $x$  where all  $y \in \bar{D}(x)$  are assigned.
- Candidate has all “preconditions” wrt.  $D$  assigned, i.e. is *enabled*.

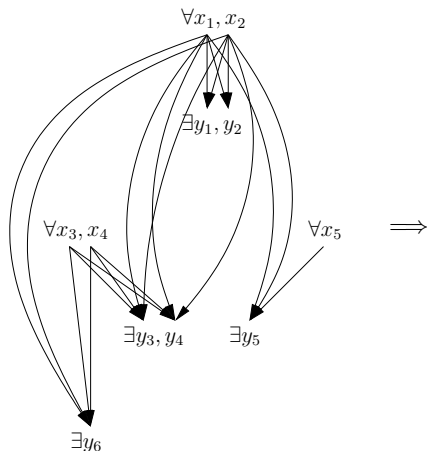
### Example

$\exists a \forall x, y \exists b. \phi$ : assigning  $a$  enables  $x$  and  $y$  by  $D^{\text{triv}}$ .

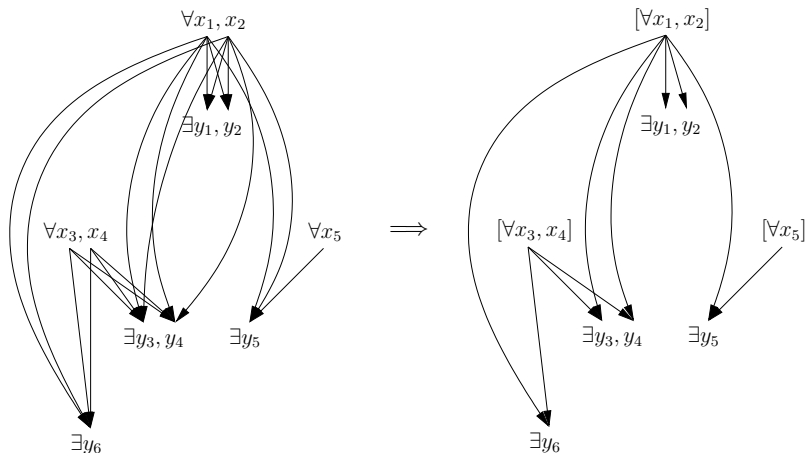
## Lazy DC-Maintenance:

- DCs needed exactly before making a decision and not e.g. during BCP.
- Defer updating set of DCs as long as possible.
- Exploit DAG structure for incremental updates.

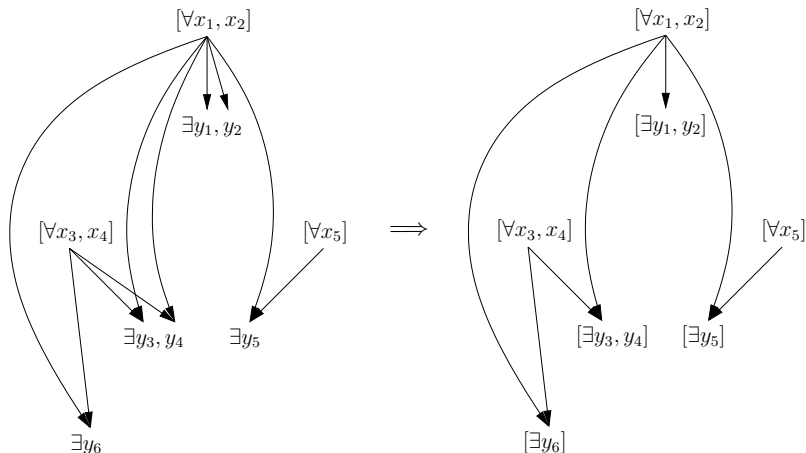




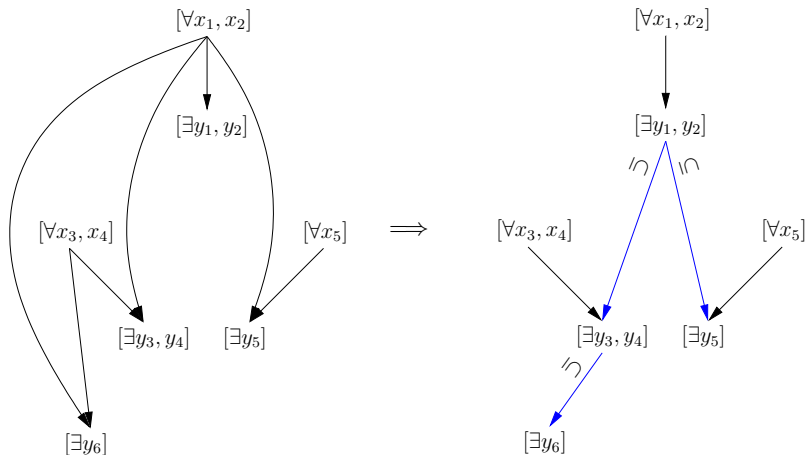
- For simplicity: ignoring dependencies of the form  $\exists x \rightarrow \forall y$ .
- DAG: edges  $x \rightarrow y$  iff  $(x, y) \in D$ .
- Construct dependency-DAG over equivalence classes of variables.



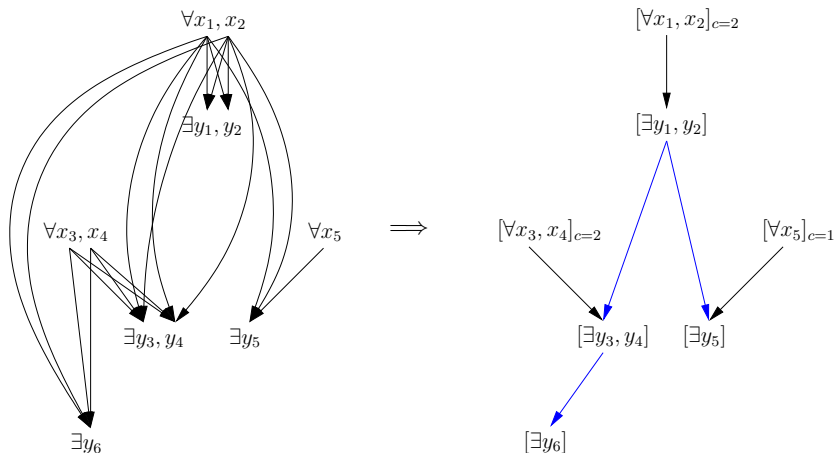
- Merge  $\forall x$  and  $\forall y$  if have same *outgoing* pointers, i.e.  $D(x) = D(y)$ .



- Merge  $\exists x$  and  $\exists y$  if have same *incoming* pointers, i.e.  $\overline{D}(x) = \overline{D}(y)$ .

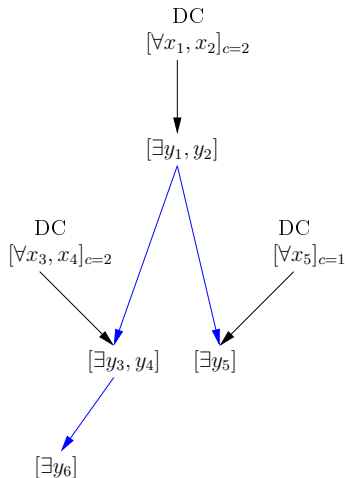


- Add non-transitive edges  $[\exists x] \rightarrow [\exists y]$  if  $\overline{D}(x) \subseteq \overline{D}(y)$ .
- Delete redundant  $\forall \rightarrow \exists$  edges.

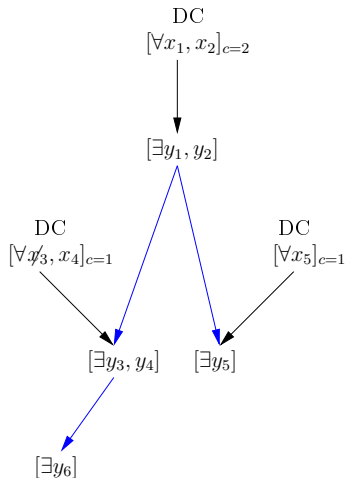


- Only fully assigned *classes* possibly enable new candidates.
- Counters  $c$  in  $\forall$ -classes: number of unassigned variables in class.
- Relevant cases:  $c = 1 \rightarrow c = 0$  and  $c = 0 \rightarrow c = 1$ .

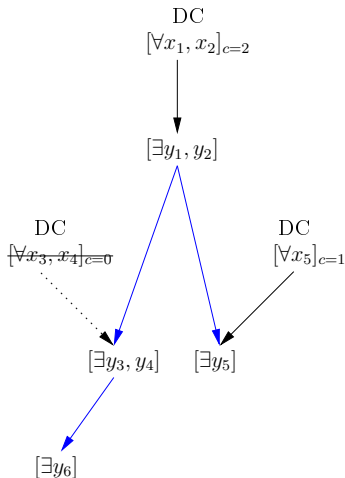
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.

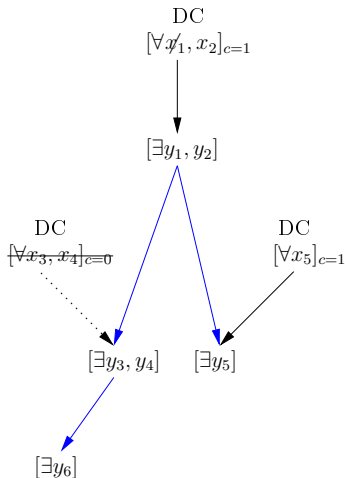


- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.

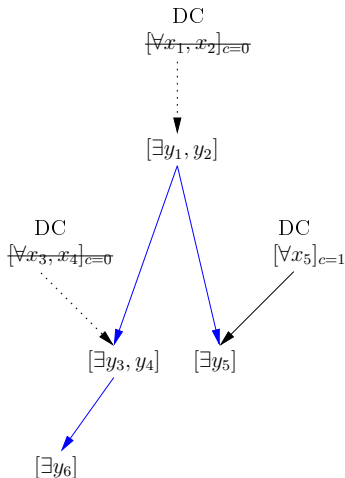




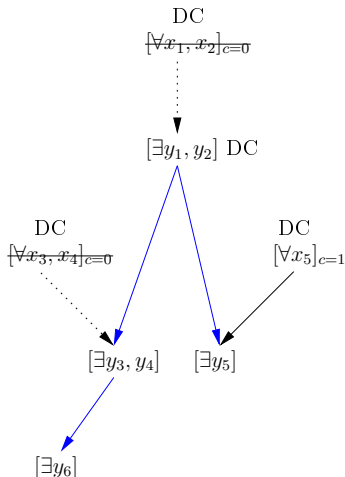
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



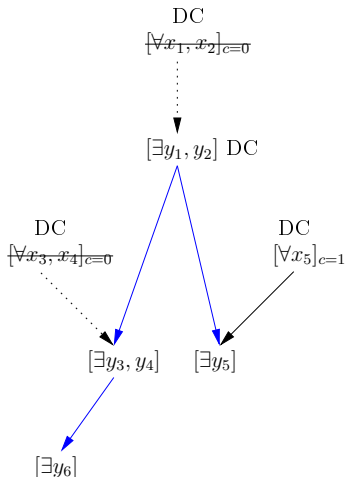
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



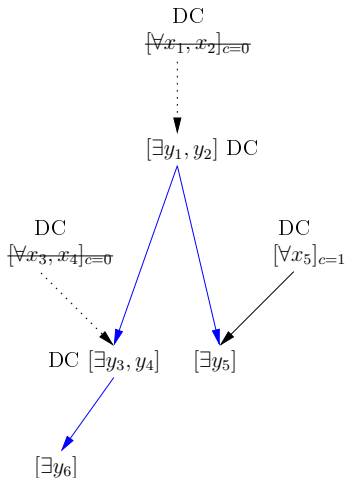
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



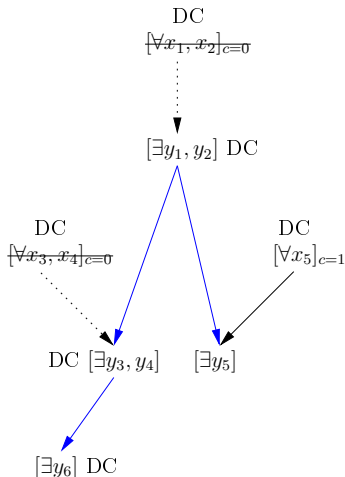
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



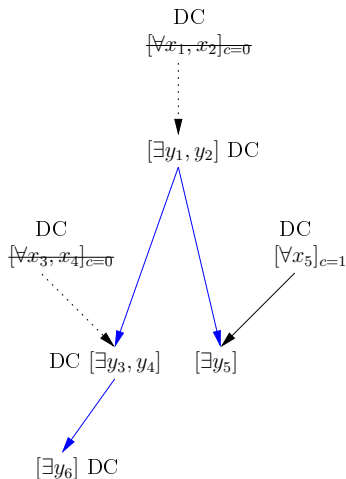
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



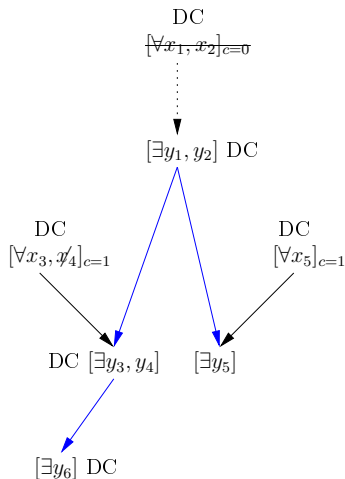
- Assign  $x_3$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_4$  still unassigned.
- Assign  $x_4$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Parent  $[\exists y_1, y_2]$  of  $[\exists y_3, y_4]$  not DC.
  - $[\exists y_1, y_2]$  has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  also ref'd by  $[\forall x_1, x_2]$ .
  - $\Rightarrow [\exists y_3, y_4]$  not DC.
- Assign  $x_1$ :  $c = 2 \rightarrow c = 1$ .
  - No change,  $x_2$  still unassigned.
- Assign  $x_2$ :  $c = 1 \rightarrow c = 0$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - $[\exists y_1, y_2]$  new DC: no unassigned  $[\forall]$ -refs.
  - $[\exists y_5]$  not DC, has unassigned  $[\forall]$ -refs.
  - $[\exists y_3, y_4]$  new DC: parent  $[\exists y_1, y_2]$  DC and no unassigned  $[\forall]$ -refs.
  - $[\exists y_6]$  new DC: parent  $[\exists y_3, y_4]$  DC and no unassigned  $[\forall]$ -refs.



- Unassign  $x_3$ :  $c = 0 \rightarrow c = 1$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Disabling  $[\exists y_3, y_4]$ , ref'd by  $[\forall x_3, x_4]$ .
  - Disabling  $[\exists y_6]$ , ref'd by  $[\exists y_3, y_4]$ .
- Unassign  $x_4$ :  $c = 1 \rightarrow c = 2$ .
  - No additional work done.

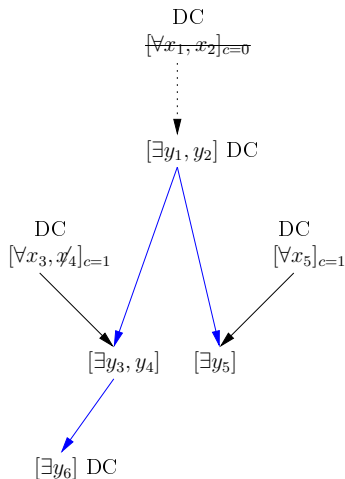


- Unassign  $x_3$ :  $c = 0 \rightarrow c = 1$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
    - Disabling  $[\exists y_3, y_4]$ , ref'd by  $[\forall x_3, x_4]$ .
    - Disabling  $[\exists y_6]$ , ref'd by  $[\exists y_3, y_4]$ .
- Unassign  $x_4$ :  $c = 1 \rightarrow c = 2$ .
  - No additional work done.

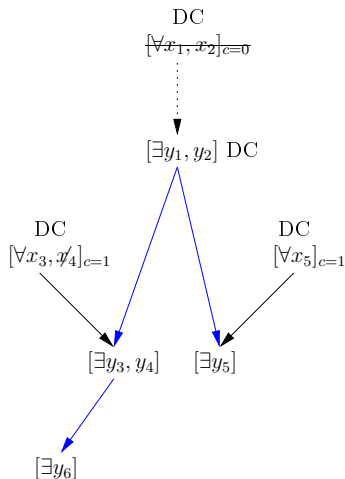




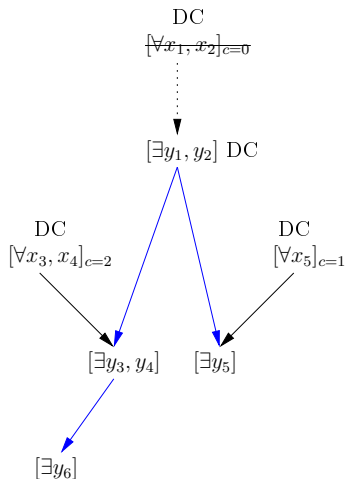
- Unassign  $x_3$ :  $c = 0 \rightarrow c = 1$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Disabling  $[\exists y_3, y_4]$ , ref'd by  $[\forall x_3, x_4]$ .
    - Disabling  $[\exists y_6]$ , ref'd by  $[\exists y_3, y_4]$ .
- Unassign  $x_4$ :  $c = 1 \rightarrow c = 2$ .
  - No additional work done.



- Unassign  $x_3$ :  $c = 0 \rightarrow c = 1$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Disabling  $[\exists y_3, y_4]$ , ref'd by  $[\forall x_3, x_4]$ .
  - Disabling  $[\exists y_6]$ , ref'd by  $[\exists y_3, y_4]$ .
- Unassign  $x_4$ :  $c = 1 \rightarrow c = 2$ .
  - No additional work done.



- Unassign  $x_3$ :  $c = 0 \rightarrow c = 1$ .
  - Follow  $[\forall] \rightarrow [\exists] \rightarrow^* [\exists]$  edges.
  - Disabling  $[\exists y_3, y_4]$ , ref'd by  $[\forall x_3, x_4]$ .
  - Disabling  $[\exists y_6]$ , ref'd by  $[\exists y_3, y_4]$ .
- Unassign  $x_4$ :  $c = 1 \rightarrow c = 2$ .
  - No additional work done.



QBF EVAL'08 (3326 formulae)					
	$D^{\text{triv}}$	$D^{\text{tree}}$	$D^{\text{std}}$	QuBE6.6-np	QuBE6.6
<i>solved</i>	1223	1221	<b>1252</b>	1106	<b>2277</b>
<i>avg. time</i>	579.94	580.64	<b>572.31</b>	608.97	<b>302.49</b>
QBF EVAL'07 (1136 formulae)					
<i>solved</i>	533	548	<b>567</b>	458	<b>734</b>
<i>avg. time</i>	497.12	484.69	<b>469.97</b>	549.29	<b>348.05</b>

Table: Comparison of DepQBF with  $D^{\text{std}} \subseteq D^{\text{tree}} \subseteq D^{\text{triv}}$  and QuBE6.6.

### DepQBF:

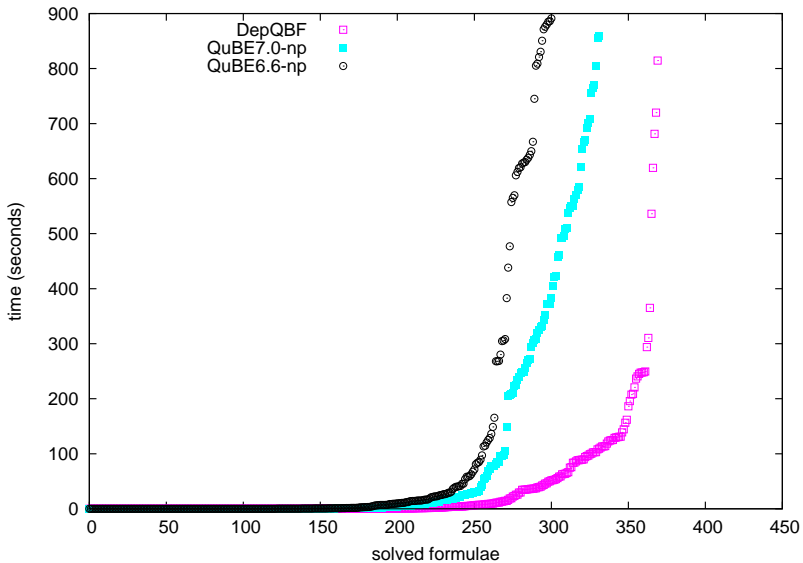
- QDPLL for PCNF with clause- and cube-learning.
- Dependency-DAG for  $D^{\text{std}}$  (primary), and  $D^{\text{tree}}$ ,  $D^{\text{triv}}$  (experimentally).
- No preprocessing.
- $D^{\text{std}}$  pays off despite DAG-overhead.
- More solved instances in less time.
- But: preprocessing is important.
- Reference: QuBE6.6 with(out) preprocessing (*QuBE6.6-np*) [GNT01].

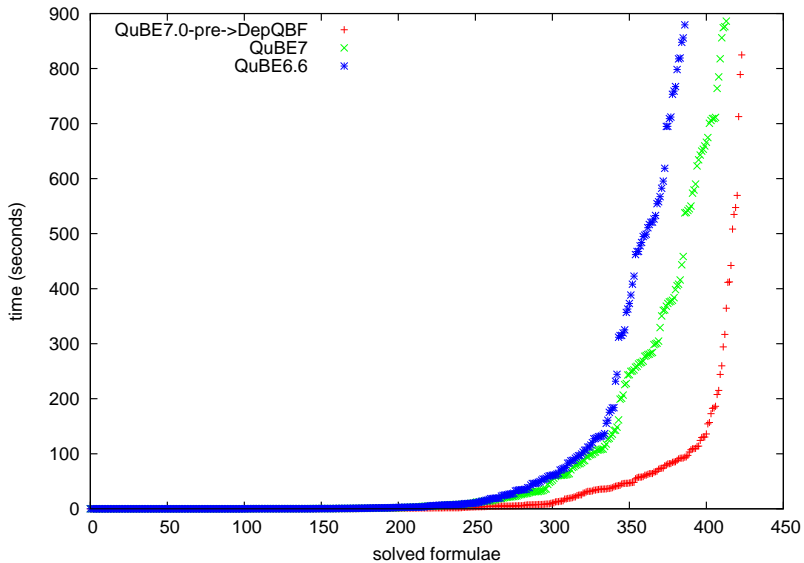
<i>QBFEVAL'10 main track (568 formulae)</i>						
	<i>Solved+Unsolved</i>		<i>Solved SAT</i>		<i>Solved UNSAT</i>	
	<i>solved</i>	<i>avg.time</i>	<i>solved</i>	<i>avg.time</i>	<i>solved</i>	<i>avg.time</i>
<b>QuBE7.0-pre<math>\rightarrow</math>DepQBF</b>	<b>424</b>	<b>254.23</b>	<b>197</b>	<b>48.17</b>	<b>227</b>	<b>23.42</b>
QuBE7	414	310.29	187	130.52	227	58.33
QuBE6.6	387	341.91	168	98.97	219	67.03
<i>without preprocessing</i>						
<b>DepQBF</b>	<b>370</b>	<b>337.10</b>	<b>165</b>	<b>54.58</b>	<b>205</b>	<b>20.82</b>
QuBE7.0-np	332	425.44	135	147.71	197	47.27
QuBE6.6-np	301	468.51	113	136.48	188	55.27

**Table:** Comparison of DepQBF with  $D^{\text{std}}$  and state-of-the-art QBF solvers. Ranking by number of solved formulae. Statistics include time for preprocessing.

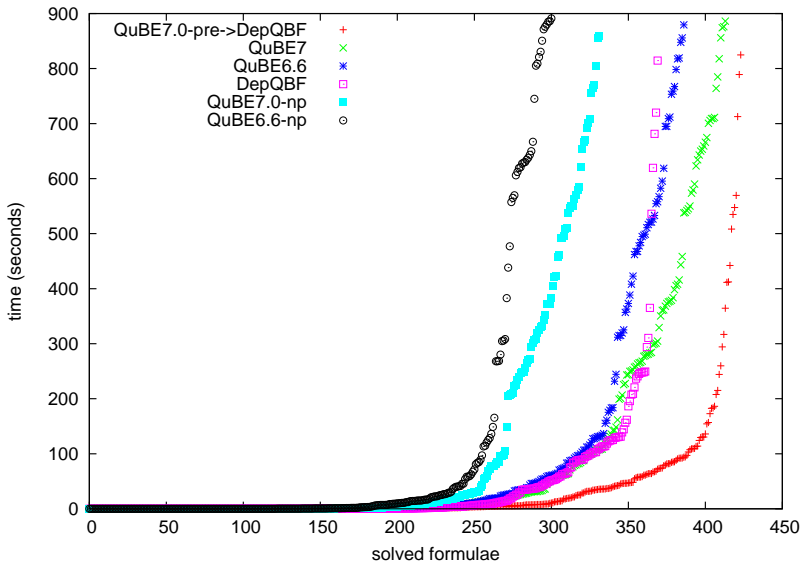
### Preprocessing:

- DepQBF with  $D^{\text{std}}$ : best both with and without preprocessing.
- QuBE7.0-pre: preprocessor integrated in QuBE7.0 [GMN10].





# Experimental Results 5/5: QBFEVAL'10





## QDPLL with Dependency Schemes:

- $D \subseteq D^{\text{triv}}$  relaxes prefix order to allow more freedom in QDPLL.

## Implementation: QDPLL-based solver DepQBF.

- Compact dependency-DAG for  $D^{\text{std}}$  over equivalence classes.
- Top-ranked solver in QBFEVAL'10:  $D^{\text{std}}$  pays off despite DAG-overhead.
- See also *Pragmatics of SAT 2010 (POS'10)* workshop:  
“DepQBF: A Dependency-Aware QBF Solver (System Description)”.

## Future Work:

- Preprocessing,...

DepQBF 0.1 is open source: <http://fmv.jku.at/depqbf/>

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
  R = get_initial_reason (s);
  // s == UNSAT: 'R' is empty clause.
  // s == SAT: 'R' is sat. cube...
  // ..or new cube from assignment.
  while (!stop_res (R))
    p = get_pivot (R);
    A = get_antecedent (p);
    R = constraint_res (R, p, A);
  add_to_formula (R);
  assign_forced_lit (R);
  return get_asserting_level (R);

```

**Figure:** QDPLL with conflict-driven clause and solution-driven cube learning.

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
  R = get_initial_reason (s);
  // s == UNSAT: 'R' is empty clause.
  // s == SAT: 'R' is sat. cube...
  // ..or new cube from assignment.
  while (!stop_res (R))
    p = get_pivot (R);
    A = get_antecedent (p);
    R = constraint_res (R, p, A);
  add_to_formula (R);
  assign_forced_lit (R);
  return get_asserting_level (R);

```

Figure: QDPLL with conflict-driven clause and solution-driven cube learning.

## Boolean Constraint Propagation (BCP):

- Assigning unit and pure literals.
- Augmented CNF:  $\phi := \phi_{OCL} \wedge \phi_{LCL} \vee \phi_{OCU}$ .
- Original clauses  $\phi_{OCL}$ , learnt clauses  $\phi_{LCL}$  and learnt cubes  $\phi_{LCU}$ .

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
  R = get_initial_reason (s);
  // s == UNSAT: 'R' is empty clause.
  // s == SAT: 'R' is sat. cube...
  // ..or new cube from assignment.
  while (!stop_res (R))
    p = get_pivot (R);
    A = get_antecedent (p);
    R = constraint_res (R, p, A);
  add_to_formula (R);
  assign_forced_lit (R);
  return get_asserting_level (R);

```

Figure: QDPLL with conflict-driven clause and solution-driven cube learning.

## Decision Making:

- BCP saturated without detecting conflict/solution.
- Select and assign one *decision candidate*.
- Candidates: according to dependency scheme and partial assignment.

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
R = get_initial_reason (s);
// s == UNSAT: 'R' is empty clause.
// s == SAT: 'R' is sat. cube...
// ..or new cube from assignment.
while (!stop_res (R))
  p = get_pivot (R);
  A = get_antecedent (p);
  R = constraint_res (R, p, A);
add_to_formula (R);
assign_forced_lit (R);
return get_asserting_level (R);

```

**Figure:** QDPLL with conflict-driven clause and solution-driven cube learning.

## Result Analysis:

- BCP detected conflict/solution.
- Conflict: empty clause.
- Solution: satisfying assignment or satisfied learnt cube.

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
  R = get_initial_reason (s);
  // s == UNSAT: 'R' is empty clause.
  // s == SAT: 'R' is sat. cube...
  // ..or new cube from assignment.
  while (!stop_res (R))
    p = get_pivot (R);
    A = get_antecedent (p);
    R = constraint_res (R, p, A);
  add_to_formula (R);
  assign_forced_lit (R);
  return get_asserting_level (R);

```

Figure: QDPLL with conflict-driven clause and solution-driven cube learning.

## Constraint Learning:

- Init. from conflict: empty clause.
- Init. from solution: sat. cube or new cube from satisfying assignment.
- Resolution/consensus: antecedents of units in current clause/cube.
- First-UIP: generalized stop criterion.

```

State qdpll ()
while (true)
  State s = bcp ();
  if (s == UNDEF)
    // Make decision.
    v = select_dec_var ();
    assign_dec_var (v);
  else
    // Conflict or solution.
    // s == UNSAT or s == SAT.
    btlevel = analyze_leaf (s);
    if (btlevel == INVALID)
      return s;
    else
      backtrack (btlevel);

DecLevel analyze_leaf (State s)
R = get_initial_reason (s);
// s == UNSAT: 'R' is empty clause.
// s == SAT: 'R' is sat. cube...
// ..or new cube from assignment.
while (!stop_res (R))
  p = get_pivot (R);
  A = get_antecedent (p);
  R = constraint_res (R, p, A);
add_to_formula (R);
assign_forced_lit (R);
return get_asserting_level (R);

```

**Figure:** QDPLL with conflict-driven clause and solution-driven cube learning.

### Backtrack:

- Assumption: learning always produces asserting constraints.
- Backtrack to asserting level.

Given dependency scheme  $D$  for PCNF. Write  $x \prec v$  if  $(x, y) \in D$ .

### Definition (Unit Clause Rule)

A clause  $C$  is *unit* if (Dual definition for cubes.)

- no literal  $l \in C$  is assigned true,
- exactly one existential literal  $l_e \in L_{\exists}(C)$  is unassigned,
- for all unassigned universal literals  $l_u \in L_{\forall}(C)$ :  $l_u \not\prec l_e$ .

**Example:**  $\exists x \forall a \exists y, z. \phi' \wedge (x \vee a \vee y \vee z)$ .

Assign  $\bar{x}, \bar{y}$ :  $\exists x \forall a \exists y, z. \phi' \wedge (x \vee a \vee y \vee z)$ .

Given  $D^{\text{triv}}$  from prefix:  $(x \vee a \vee y \vee z)$  *not* unit since  $a \prec z$  (because  $\forall a$  before  $\exists z$ ).

Given  $D \subseteq D^{\text{triv}}$  where  $a \not\prec z$ :  $(x \vee a \vee y \vee z)$  unit.

### Practical Effects:

- Expecting more units when using  $D \subseteq D^{\text{triv}}$ .
- Combining two-literal watching with dependency checking.



**Constraint Reduction:** universal/existential reduction of clauses/cubes.

### Definition (Universal Reduction of Clauses)

A universal literal  $l_u \in L_{\forall}(C)$  can be deleted from a clause  $C$  iff

- there is no  $l_e \in L_{\exists}(C)$  with  $l_u \prec l_e$ .
- The result of saturated universal reduction is denoted by  $CR(C)$ .

(Dual definition of existential reduction for cubes.)

**Example:**  $\exists x \forall a \exists y. \phi' \wedge (x \vee a \vee y)$ .

Given  $D^{\text{triv}}$  from prefix:  $a$  is irreducible in  $(x \vee a \vee y)$  since  $a \prec y$ .

Given  $D \subseteq D^{\text{triv}}$  where  $a \not\prec y$ :  $a$  is reducible in  $(x \vee a \vee y)$ , yielding  $(x \vee y)$ .

### Practical Effects:

- Expecting shorter learnt constraints when using  $D \subseteq D^{\text{triv}}$ .
- Combining constraint reduction with dependency checks.

**Constraint Resolution:** Q-resolution/consensus of clauses/cubes.

### Definition (Q-resolution for Clauses)

*Clarifies Def. 7 in paper.*

Let  $C_1, C_2$  be clauses with  $v \in L_{\exists}(C_1), \bar{v} \in L_{\exists}(C_2)$ .

- 1  $C := (CR(C_1) \cup CR(C_2)) \setminus \{v, \bar{v}\}$ .
- 2 If  $C$  contains complementary literals then no resolvent exists.
- 3 Otherwise, resolvent  $C' := CR(C)$  of  $C_1$  and  $C_2$  on  $v$ :  $\{C_1, C_2\} \vdash_v C'$ .

(Dual definition of consensus for cubes.)

**Example:**  $\exists x \forall a \exists y, z. \phi' \wedge (x \vee a \overset{C_1}{\vee} y \vee z) \wedge (x \vee a \overset{C_2}{\vee} y \vee \bar{z}) \wedge (x \vee \bar{a} \overset{C_3}{\vee} \bar{y} \vee z)$ .

Given  $D^{\text{triv}}$  from prefix:  $\{C_1, C_2\} \vdash_z (x \vee a \vee y)$ , but  $\{(x \vee a \vee y), C_3\} \not\vdash_y$ .

Given  $D \subseteq D^{\text{triv}}$  where  $a \not\prec y$ :  $\{C_1, C_2\} \vdash_z (x \vee y)$ , and  $\{(x \vee y), C_3\} \vdash_y (x \vee \bar{a} \vee z)$ .

### Practical Effects:

- Possible reductions of “resolution-blocking” literals when using  $D \subseteq D^{\text{triv}}$ .

### Definition (Asserting Clause/Level)

Clarifies Def. 8 from paper. See also function `get_reason_asserting_level` in `DepQBF 0.1` source code.

Let  $R$  be a resolvent i.e.  $\{\dots\} \vdash^* R$ . Let  $d := \max(\{dl(I) \mid I \in L_{\exists}(R)\})$ .  $R$  is asserting at  $a := \max(\{dl(I) < d \mid I \in L_{\exists}(R) \text{ or } I \in L_{\forall}(R) \text{ with } I \prec d\})$  iff

- 1 the decision variable at level  $d$  is existential,
- 2 there is exactly one  $I \in L_{\exists}(R)$  with  $dl(I) = d$ ,
- 3 for all  $I_u \in L_{\forall}(R)$  where  $I_u \prec I$ :  $I_u$  must be assigned false with  $dl(I_u) < d$ .

(Dual definition for asserting cubes.)

**Example:**  $\dots \exists x \dots \forall a \dots \exists y, z \dots \phi' \wedge (x \vee a \vee y \vee z)$ .

Given  $D^{\text{triv}}$  from prefix: in  $(x^{\textcircled{1}} \vee a^{\textcircled{3}} \vee y^{\textcircled{2}} \vee z^{\textcircled{4}})$ ,  $z$  is unit at level 3.

Given  $D \subseteq D^{\text{triv}}$  where  $a \not\prec z$ : in  $(x^{\textcircled{1}} \vee a^{\textcircled{3}} \vee y^{\textcircled{2}} \vee z^{\textcircled{4}})$ ,  $z$  is unit at level 2.

### Practical Effects:

- Possibly longer backjumps when using  $D \subseteq D^{\text{triv}}$ .



U. Bubeck and H. Kleine Büning.

Bounded Universal Expansion for Preprocessing QBF.

In J. Marques-Silva and K. A. Sakallah, editors, *SAT*, volume 4501 of *LNCS*, pages 244–257. Springer, 2007.



M. Benedetti.

Quantifier Trees for QBFs.

In F. Bacchus and T. Walsh, editors, *SAT*, volume 3569 of *LNCS*, pages 378–385. Springer, 2005.



A. Biere.

Resolve and Expand.

In H. H. Hoos and D. G. Mitchell, editors, *SAT (Selected Papers)*, volume 3542 of *LNCS*, pages 59–70. Springer, 2004.



H. Kleine Büning, M. Karpinski, and A. Flögel.

Resolution for Quantified Boolean Formulas.

*Inf. Comput.*, 117(1):12–18, 1995.



M. Cadoli, A. Giovanardi, and M. Schaerf.

An Algorithm to Evaluate Quantified Boolean Formulae.

In *AAAI/IAAI*, pages 262–267, 1998.



E. Giunchiglia, P. Marin, and M. Narizzano.

sQueueBF: An Effective Preprocessor for QBFs.

In O. Strichman and S. Szeider, editors, *SAT (accepted for publication)*, LNCS. Springer, 2010.



E. Giunchiglia, M. Narizzano, and A. Tacchella.

QUBE: A System for Deciding Quantified Boolean Formulas Satisfiability.

In R. Goré, A. Leitsch, and T. Nipkow, editors, *IJCAR*, volume 2083 of LNCS, pages 364–369. Springer, 2001.



E. Giunchiglia, M. Narizzano, and A. Tacchella.

Learning for Quantified Boolean Logic Satisfiability.

In *AAAI/IAAI*, pages 649–654, 2002.



E. Giunchiglia, M. Narizzano, and A. Tacchella.

Clause/Term Resolution and Learning in the Evaluation of Quantified Boolean Formulas.

*J. Artif. Intell. Res. (JAIR)*, 26:371–416, 2006.



E. Giunchiglia, M. Narizzano, and A. Tacchella.

Quantifier Structure in Search-Based Procedures for QBFs.

*TCAD*, 26(3):497–507, 2007.



F. Lonsing and A. Biere.

A Compact Representation for Syntactic Dependencies in QBFs.

In O. Kullmann, editor, *SAT*, volume 5584 of LNCS, pages 398–411. Springer, 2009.



F. Lonsing and A. Biere.

DepQBF: A Dependency-Aware QBF Solver (System Description).

In A. Van Gelder and D. Le Berre, editors, *Pragmatics of SAT Workshop (POS)*, accepted for publication, 2010.



R. Letz.

Lemma and Model Caching in Decision Procedures for Quantified Boolean Formulas.

In U. Egly and C. G. Fermüller, editors, *TABLEAUX*, volume 2381 of *LNCS*, pages 160–175. Springer, 2002.



M. Samer and S. Szeider.

Backdoor Sets of Quantified Boolean Formulas.

*Journal of Automated Reasoning (JAR)*, 42(1):77–97, 2009.



L. Zhang and S. Malik.

Towards a Symmetric Treatment of Satisfaction and Conflicts in Quantified Boolean Formula Evaluation.

In P. Van Hentenryck, editor, *CP*, volume 2470 of *LNCS*, pages 200–215. Springer, 2002.